



INSTALLATION INSTRUCTIONS

1. Remove the cover plate from the appropriate option slot in the ATLAS 550 rear panel.
2. Slide the Dual USSI Module into the option slot until the module is firmly positioned against the front of the chassis.
3. Secure the thumbscrews at both edges of the module. Tighten with a screwdriver.
4. Connect the cable to the associated device(s).
5. Complete installation of remaining modules and Base Unit as specified in the Installation chapter of the ATLAS 550 System Manual.

SPECIFICATIONS

DTE Interface	EIA-530 Synchronous EIA-530A Synchronous RS-449 RS-232 Synchronous CCITT X.21 (V.11)
Rate	56 kbps to 2.048 Mbps in 56K or 64K steps
Tests	Local Loopback (bilateral) Remote Loopback (V.54) Self-test
Test Pattern	511 with errored seconds display and error inject capability
Excessive 0s Protection	Excessive zeros alarm indication and configurable excessive zeros protection
Connector	DB-78 with cable to convert to D sub female connectors

MENU TREE

Modules	Slit	DTE Status	Name	
	Type	Data Rate	Clk (+/-)	
	Menu	Inband Stats	Data	
	Alarms	PLL/Fifo	CTS	
	Test	Configuration	DCD	
	State		DSR	
	Status		DTR	
	Rev		0 Inh	
		Dial	Inband	Send Leads
			SRC ID	Loopbk
		Number	Loopback Status	
	Test		511	
			511 Result	
			Inject	
			511 Clr	
	DTE Interface		DTE Interface Mode	
			Current DTE Type	

EIA-530 PINOUT

P/N: 4200754L2

Pin	Signal Description	Pin	Signal Description
1	Shield (Ground)	13	Clear to Send (B)
2	Transmit Data (A)	14	Transmit Data (B)
3	Received Data (A)	15	Transmit Clock (A)
4	Request to Send (A)	16	Received Data (B)
5	Clear to Send (A)	17	Receive Clock (A)
6	DCE Ready (A)	18	Local Loopback*
7	Signal Ground	19	Request to Send (B)
8	Carrier Detect (A)	20	DTE Ready (A)
9	Received Clock (B)	21	Remote Loopback*
10	Carrier Detect (B)	22	DCE Ready (B)
11	Ext. Transmit Clock (B)	23	DTE Ready (B)
12	Transmit Clock (B)	24	Ext. Transmit Clock (A)
		25	Test Mode*

* Ignored by Dual USSI Module

EIA-530A PINOUT

P/N: 4200754L2

Pin	Signal Description	Pin	Signal Description
1	Shield (Ground)	13	Clear to Send (B)
2	Transmit Data (A)	14	Transmit Data (B)
3	Received Data (A)	15	Transmit Clock (A)
4	Request to Send (A)	16	Received Data (B)
5	Clear to Send (A)	17	Receive Clock (A)
6	DCE Ready (A)	18	Local Loopback*
7	Signal Ground	19	Request to Send (B)
8	Carrier Detect (A)	20	DTE Ready (A)
9	Received Clock (B)	21	Remote Loopback*
10	Carrier Detect (B)	22	Ring Indicator
11	Ext. Transmit Clock (B)	23	Signal Ground
12	Transmit Clock (B)	24	Ext. Transmit Clock (A)
		25	Test Mode*

* Ignored by Dual USSI Module

CCIT X.21 (V.11) PINOUT

P/N: 4200754L3

Pin	Signal Description	Pin	Signal Description
1	Shield (Ground)	8	Signal Ground
2	Transmit Data (A)	9	Transmit Data (B)
3	Request to Send (A)	10	Request to Send (B)
4	Received Data (A)	11	Received Data (B)
5	Carrier Detect (A)	12	Carrier Detect (B)
6	Transmit/Receive Clock (A)	13	Transmit/Received Clock (B)
7	Ext. Transmit Clock (A)	14	Ext. Transmit Clock (B)
15	Not Used		

DB-78 PINOUT

Pin	Signal Description	Pin	Signal Description
1	RXD-A 2	41	RTS-B 2
2	RXD-B 2	42	GND
3	RXC-A 2	43—48	NOT USED
4	RXC-B 2	49	MOD2
5	TXD-A 2	50	MOD0
6	TXD-B 2	51	EXT-TXC-A 1
7	TXC-A 2	52	DTR-B 1
8	TXC-B 2	53	DTR-A 1
9	EXT-TXC-A 2	54	DCD-B 1
10	EXT-TXC-B 2	55	DCD-A 1
11—17	NOT USED	56	DSR-B/RI 1
18	GND	57	DSR-A 1
19	GND	58	CTS-B 1
20	CHASIS GND	59	CTS-A 1
21	CTS-A 2	60	CHASIS GND
22	CST-B 2	61	GND
23	DSR-A 2	62—68	NOT USED
24	DSR-B/RI 2	69	MOD1
25	DCD-A 2	70	EXT-TXC-B 1
26	DCD-B 2	71	TXC-B 1
27	DTR-A 2	72	TXC-A 1
28	DTR-B 2	73	TXD-B 1
29—37	NOT USED	74	TXD-A 1
38	RTS-A 1	75	RXC-B 1
39	RTS-B 1	76	RXC-A 1
40	RTS-A 2	77	RXD-B 1
		78	RXD-A 1

RS-449 PINOUT

P/N: 4200754L1

Pin	Signal Description	Pin	Signal Description
1	Shield (Ground)	19	Signal Ground
2	Signaling Rate Indicator*	20	Receive Common*
3	Not Used	21	Not Used
4	Transmit Data (A)	22	Transmit Data (B)
5	Transmit Clock (A)	23	Transmit Clock (B)
6	Received Data (A)	24	Receive Data (B)
7	Request to Send (A)	25	Request to Send (B)
8	Receive Clock (A)	26	Receive Clock (B)
9	Clear to Send (A)	27	Clear to Send (B)
10	Local Loopback*	28	Terminal in Service*
11	DCE Ready (A)	29	DCE Ready (B)
12	DTE Ready (A)	30	DTE Ready (B)
13	Carrier Detect (A)	31	Carrier Detect (B)
14	Remote Loopback*	32	Select Standby*
15	Ring Indicator	33	Signal Quality*
16	Select Frequency*	34	New Signal*
17	Ext. Transmit Clock (A)	35	Ext. Transmit Clock (B)
18	Test Mode*	36	Standby/Indicator*
		37	Send Common*

* Ignored by Dual USSI Module

RS-232 PINOUT

P/N: 4200754L4

Pin	Signal Description	Pin	Signal Description
1	Shield (Ground)	14	Sec. Transmit Data
2	Transmit Data	15	DCE Transmit Clock
3	Received Data	16	Sec. Received Data
4	Request to Send	17	Receive Signal Element Timing
5	Clear to Send	18	Not used
6	Data Set Ready	19	Sec. Request to Send
7	Signal Ground	20	Data Terminal Ready
8	Received Line Signal Detector	21	Signal Quality Detector*
9	+ Voltage*	22	Ring Indicator
10	- Voltage*	23	Data Signal Rate Selector*
11	Not used	24	DTE Transmit Clock
12	Sec. Received Line Signal Indicator	25	Not used
13	Sec. Clear to Send		

* Ignored by Dual USSI Module