



CONNECTION PINOUTS

EIA-232 Pin	V.35 Pin	CCITT V.24	Description
-	A	101	Protective ground (PG)
7	B	102	Signal ground (SG)
4	C	105	Request to send (RTS) from DTE
5	D	106	Clear to send (CTS) to DTE
6	E	107	Data set ready (DSR) to DTE
8	F	109	Received line signal detector (DCD) to DTE
20	H	108/2	Data terminal ready (DTR) from DTE
22	J	125	Ring indicator (RI) to DTE
3	R	104	Received data (RD-A) to DTE
3	T	104	Received data (RD-B) to DTE
17	V	115	RX clock (RC-A) to DTE
17	X	115	RX clock (RC-B) to DTE
2	P	103	Transmitted data (TD-A) from DTE
2	S	103	Transmitted data (TD-B) from DTE
15	Y	114	TX clock (TC-A) to DTE
15	AA	114	TX clock (TC-B) to DTE
24	U	113	External TX clock (ETC-A) from DTE
24	W	113	External TX clock (ETC-B) from DTE
-	NN	142	Test mode (TM) to DTE

INSTALLATION INSTRUCTIONS

1. Remove the cover plate from the appropriate option slot in the ATLAS 550 Base Unit.
2. Slide the Legacy Data Module into the option slot until the module is firmly positioned against the front of the chassis.
3. Secure the thumbscrews at both edges of the module. Tighten with a screwdriver.
4. Connect the cables to the associated device(s).
5. Complete installation of remaining modules and Base Unit as specified in the Installation chapter of the ATLAS 550 System Manual.

SPECIFICATIONS

Interfaces	4 EIA-232 or (optional) V.35 (using adapter cables)
Rates	2400 to 64k baud per channel (synchronous) 2400 to 115.2k baud per channel (asynchronous)
Protocols	SDLC, Frame Relay, Transparent, PPP
Tests	DTE Loopback
Connectors	DB-78 with supplied cables to convert to EIA-232 female or (optional) V.35 Winchester female connectors
Approvals	FCC Part 15, Class A CSA 950 3rd Edition UL 60950

MENU TREE

