



T200 HDSL4 Transceiver Unit for the Remote End Installation and Maintenance Practice

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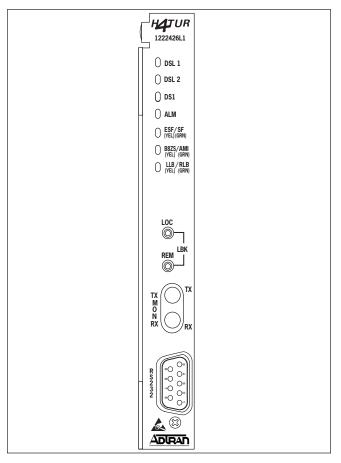


Figure 1. ADTRAN T200 H4TU-R

1. GENERAL

The ADTRAN 4-wire T200 HDSL4 transceiver unit for the remote end (H4TU-R), P/N 1222426L1, is a network terminating unit used to deploy an HDSL4 T1 circuit using 4-wire metallic facilities, see **Figure 1**. The T200 H4TU-R is a T200 mechanics card which will fit T200 or T400 mechanic enclosures. The H4TU-R can be housed in the ADTRAN standalone metal enclosures (P/N 1242034Lx or 1245034L1). Refer to the appropriate ADTRAN practice for more information. The T200 H4TU-R card can also plug into the ADTRAN HR12 HDSL4 remote shelf (P/N 1242007Lx), or the ADTRAN HR4 HDSL4 remote shelf (P/N 1242008L1).

The T200 H4TU-R works with multiple list versions of the HDSL4 transceiver unit for the central office (H4TU-C) and repeater (H4R) as listed below.

Unit Number	Description
118141xL4	Total Access 3000 H4TU-C
118141xL5	Total Access 3000 H4TU-C
	with Protection Switching
122x401L42	220/E220 H4TU-C
122x403L4I	DDM+ H4TU-C
122x404L43	3192 H4TU-C
122x407L4I	H4TU-C for Soneplex
122x441L1	T200 H4R
122x445L1	239 H4R

Note: x = any generic number

The T200 H4TU-R can be deployed in circuits using one H4TU-C, one H4TU-R, and up to two H4Rs.

The T200 H4TU-R terminates local loop HDSL4 signals originating from the Central Office (CO) unit and transforms the HDSL4 signal into traditional DS1 signals to be delivered to the customer.

The T200 H4TU-R (P/N 122X426L7) can be used with any H4TU-C to complete a fully span-powered HDSL4 circuit with up to two H4Rs. Span power is provided from the H4TU-C.

NOTE

This unit is intended for span power only. If a locally powered unit is needed, refer to P/N 122X424L7.

Revision History

This is the second release of this document. This revision is to update the screens.

2. INSTALLATION



After unpacking the unit, inspect it for damage. If damage is discovered, file a claim with the carrier, then contact ADTRAN. For more information, refer to the *Warranty and Customer Service section*.

The settings on the H4TU-C are encoded and transmitted to the H4TU-R once the circuit has achieved synchronization. There are no switch settings on the H4TU-R.

Remote Provisioning

This H4TU-R can be used to provision the entire HDSL4 circuit via the craft interface.

Compliance

Table 1 shows the compliance codes for the H4TU-R. This product is intended for installation in equipment with a Type "B" or "E" enclosure.

Table 1. Compliance Codes

Code	Input	Output
Power Code (PC)	C	C
Telecommunication Code (TC)	X	X
Installation Code (IC)	A	_

WARNING

Up to -200 VDC may be present on telecommunications wiring. Ensure chassis ground is properly connected.

This product meets all requirements of Bellcore GR-1089-CORE (Class A2), ANSI T1.418-2002 and is NRTL listed to the applicable UL standards.

Front Panel Indicators

There are seven front panel status LEDs. Each indicator is described in **Table 2**.

Table 2. Front Panel Indicators

H4TUR 1222426L1
DSL 1 DSL 2 DS1 ALM ESF/SF (YEL/(GRN) BEZ/S/AMI (YEL) (GRN) LLB / RLB (YEL) (GRN)
LOC BER LBK SO TX OO N RX
R SOCION SE

Name	Indication	Description				
DSL 1	Green	DSL Loop 1 sync, no errors currently detected, and signal margin ≥ 3 dB				
	Red	To DSL Loop 1 sync, errors being detected, or signal margin < 3 dB				
DSL 2	Green	OSL Loop 2 sync, no errors currently detected, and signal margin ≥ 3 dB				
	Red	No DSL Loop 2 sync, errors being detected, or signal margin < 3dB				
DS1	Green	SX-1 signal is present and no errors currently being detected				
	Red	No DSX-1 signal or signal is present with errors				
ALM	Off	o active alarm present				
	Red	Loss of DSX-1 signal to the unit				
	Yellow	Loss of DS1 signal to the remote				
ESF/SF	Off	Unit has detected UNFRAMED data				
	Yellow	Unit has detected ESF data				
	Green	Unit has detected SF data				
B8ZS/AMI	Yellow	Unit has detected B8ZS coded data				
	Green	Unit has detected AMI data				
LLB/RLB	Off	Unit is NOT in loopback				
	Yellow	This unit is in loopback				
	Green	The H4TU-C is in Loopback toward the customer				

3. CONNECTIONS

All connections of the H4TU-R are made through card edge connectors. **Figure 2** gives the card edge pin assignments for the H4TU-R circuit pack.

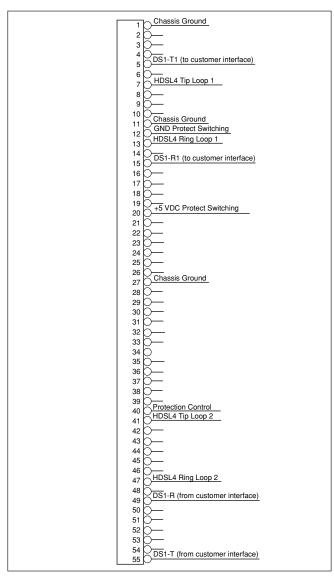


Figure 2. H4TU-R Edge Connector Wiring

When the circuit pack is installed in any of the H4TU-R enclosures, all connections are made through the enclosure backplanes. See the following ADTRAN documents for more information:

Document No.	Description
61242007LX-5	HR12 I&M
61242008L1-5	HR4 I&M
61242034L2-5	T400 Single Mount I&M
	(removable RJ-48 jacks)
61242034L3-5	T400 Single Mount HV I&M
61245034L1-5	T200 Dual Mount I&M

CAUTION

Ensure chassis ground is properly connected for either standalone or shelf-mounted applications.

4. HDSL4 SYSTEM TESTING

The T200 H4TU-R provides diagnostic, loopback, and signal monitoring capabilities.

The seven front panel LEDs provide diagnostics for HDSL4 loops, DS1 signals, alarms, provisioning, and loopbacks. Refer to Section 2, *Installation* for details.

The H4TU-R provides a bidirectional loopback via the loopback button on the front panel. Refer to *H4TU-R Network Loopbacks* and *Customer Loopbacks* sections for more details.

The H4TU-R also provides a nonintrusive test point of the DS1 signal via the jack labeled **MON** on the front panel.

DS1 MON Bantam Jacks

The **MON** jack provides a nonintrusive access point for monitoring the characteristics of the transmit and receive signals at the DS1 interface point.

For example, the DS1 **MON** jack on the H4TU-R could be used to connect to a bit error rate tester to monitor for synchronization, test patterns, etc. **Figure 3** is an illustration of specific jack detail.

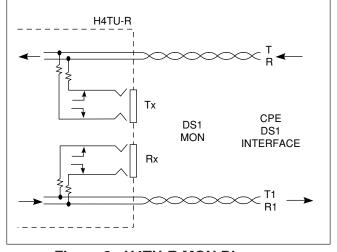


Figure 3. H4TU-R MON Diagram

NOTE

For the MON jacks, the Tx and Rx indications relate to the direction of the CPE signal.

H4TU-R Network Loopbacks

See **Figure 4**. The H4TU-R responds to multiple loopback activation processes as follows. The loopback position is a logic loopback located within the H4TU-R internal HDSL4 transceiver.

First, manual loopback activation may be accomplished using the control port of the H4TU-R.

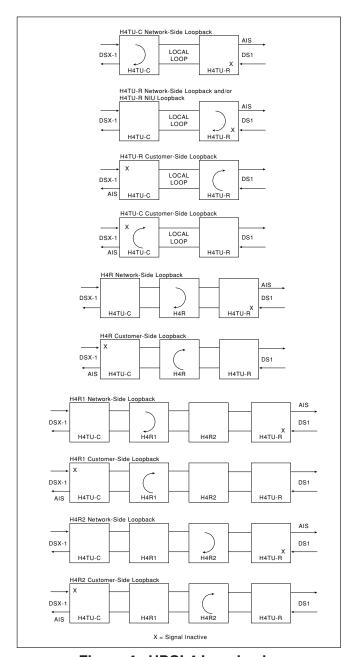


Figure 4. HDSL4 Loopbacks

Second, the H4TU-R will respond to the industry HDSL loopback codes as designated in the ANSI document T1E1.4/92. A synopsis of the method described by ANSI is presented in *Appendix A*.

Third, the H4TU-R responds to T1 Network Interface Unit (NIU) loopback codes as described in Bellcore TR-TSY-000312. The NIU loopback codes are as follows:

In-Band Codes

Loop up 11000

Loop down.. 11100

ESF Codes

Loop up 1111 1111 0100 1000 Loop down .. 1111 1111 0010 0100

Receiving the in-band codes for more than five seconds or the ESF codes four consecutive times will cause the appropriate loopback action. The ESF codes must be transmitted in the Facility Data Link (FDL).

NOTE

The NIU loopback option must be enabled before the H4TU-R can respond to the NIU loopback.

The H4TU-R will respond to the loop up codes by activating the NIU loopback from either the disarmed or armed state. The loop down codes will return the H4TU-R to the state from the armed or loop up state.

Refer to *Appendix A* for more details on loopbacks and loopback arming sequences.

Figure 4 illustrates all of the possible loopback locations of the ADTRAN HDSL4 equipment.

Customer Loopbacks

In addition to the loopbacks in the direction of the network, the H4TU-R may also be looped back in the direction of the customer. The H4TU-C and H4TU-R customer-side loopbacks are illustrated in Figure 4.

NOTE

Network and customer loopbacks are governed by the loopback time out option. (Default=120 minutes).

5. FRONT PANEL OPERATION

The front panel contains two pushbuttons. These are labeled **LOC** and **REM**.

The LOC pushbutton controls a bidirectional loopback at the H4TU-R. Pressing the button causes a bidirectional loopback to occur. If the bidirectional loopback is active, pressing the button a second time will disable the loopback.

The REM pushbutton controls a loopback at the H4TU-C. Pressing the button causes a loopback toward the H4TU-R to occur. If the loopback is active, pressing the button a second time will disable the loopback.

6. CONTROL PORT OPERATION

The H4TU-R provides a front panel-mounted DB-9 connector that supplies an RS-232 interface for connection to a controlling terminal. The pinout of the DB-9 is illustrated in **Figure 5**.

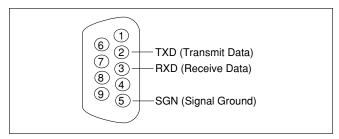


Figure 5. RS-232 (DB-9) Connector Pinout

The terminal interface operates at data rates from 1.2, 2.4, 4.8, 9.6, and 19.2 kbps. The asynchronous data format is fixed at 8 data bits, no parity, and 1 stop bit.

NOTE

If using a personal computer with terminal emulation capability, be sure to disable any power-saving programs. Otherwise, communication between the PC and the HDSL4 unit may be disrupted, resulting in misplaced characters or screen time outs.

Operation

For abbreviations used in the screen diagrams, see **Table 3**.

Table 3. Screen Abbreviations

Abbreviation	Definition					
ES	. Errored Seconds DSX/DS1(SF) Second in which BPVs or frame bit error occurs (ESF) Second in which a BPV or CRC error occurs HDSL4Second in which a CRC error occurs					
SES	DSX/DS1					
UAS	. Unavailable Seconds DSX/DS1 Second in which there is a loss of signal or sync HDSL4 Second in which there is a loss of signal or sync					
SF	. Superframe Format					
ESF	. Extended Superframe Format					
B8ZS	. Bipolar with 8 Zero Substitution					
AMI	. Alternate Mark Inversion					
LBO	. Line Build Out					
BPV	. Bipolar Violation DSX/DS1 Second in which a bipolar violation occurs					
NIU	. T1 Network Interface Unit					
S/N	. Serial Number					
15M	. 15-Minute period					
24H	. 24-Hour period					

The screens illustrated in Figures 6 through 22 apply to an HDSL4 circuit deployed with ADTRAN's HDSL4 technology. The circuit includes an H4TU-C, up to two H4Rs and an H4TU-R. Other configurations are possible (for example, HDSL4 repeater, other vendor's equipment) and their displays will vary slightly from those shown in this section.

A terminal session is initiated by entering multiple space bar characters which are used by the H4TU-R to determine the speed of the terminal. Once the speed has been determined, an HDSL4 Main Menu is presented as illustrated in **Figure 6**.

The Main Menu provides access to detailed performance and configuration information. Selecting the corresponding number or letter can access the following screens:

- 1. HDSL4 Unit Information
- 2. Provisioning
- 3. Span Status
- 4. Loopbacks and Test
- 5. Performance History
- 6. Scratch Pad, Circuit ID, Time/Date
- 7. Terminal Modes
- 8. Alarm History
- 9. Event History
- 10. System PM/Screen Report
- 11. Virtual Terminal Control

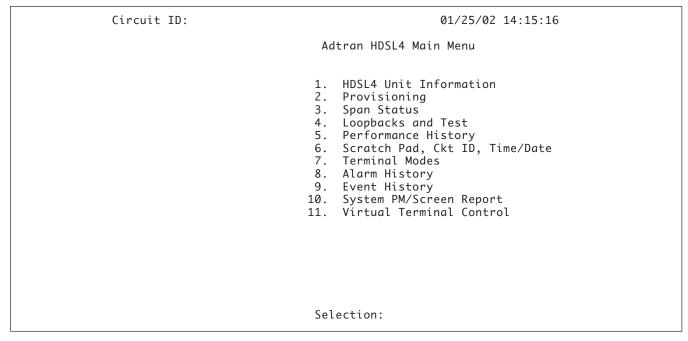


Figure 6. HDSL4 Main Menu

The HDSL4 Unit Information screen, illustrated in **Figure 7**, provides detailed product information on each component in the HDSL4 circuit. This screen also displays contact information for ADTRAN Technical Support, Internet site, and address.

The Provisioning screen, illustrated in **Figure 8**, displays the current provisioning settings for the HDSL4 circuit. To change a particular option setting, select the appropriate number, and a new menu will appear with a list of the available settings.

NOTE

The provisioning options listed in Figure 8 are the factory default values.

```
CIRCUIT ID:
                                                                          01/25/02 11:52:00
                          PRESS ESC TO RETURN TO PREVIOUS MENU
                                         ADTRAN
                                 901 EXPLORER BOULEVARD
                            HUNTSVILLE, ALABAMA 35806-2807
                ------ FOR INFORMATION OR TECHNICAL SUPPORT ------
SUPPORT HOURS ( NORMAL 7AM - 7PM CST, EMERGENCY 7 DAYS X 24 HOURS ) PHONE: 800.726.8663 / 888.873.HDSL FAX: 256.963.6217 INTERNET: WWW.ADTRAN.COM
                ADTN H4TU-C
                                                         ADTN H4TU-R
                P/N: 1222401L1
S/N: 123456789
                                                         P/N: 1222426L1
S/N: 123456789
               CLEI: T1L5TWPCAA
                                                        CLEI: T1L5JZTCAAA
               MANF: 01/01/2000
                                                        MANF: 01/01/2000
                VER: 24 1 A00000
                                                         VER: 27 2 A00000
               ADTN H4R1
                                                         ADTN H4R2
               P/N: 1221445L1
                                                         P/N: 1221445L1
               S/N: BB50A8343
                                                         S/N: BB50A8353
              CLEI: T1R5YP3DAA
                                                        CLEI: T1R5YP3DAA
              MANF: 02/12/2002
                                                        MANF: 02/12/2002
```

Figure 7. Unit Information Screen

```
Circuit ID:
                                                   01/25/02 11:15:25
                                   Provisioning
                            DSX-1 Line Buildout
                                                      = 0-133 Feet
                        2. DSX-1/DS1 Line Code
                                                      = B8ZS
                        3. DSX-1/DS1 Framing
                                                      = ESF
                        4. Forced Frame Conversion = Disabled
                        5. Smartjack Loopback = Enabled
                            Loopback Timeout = 120 Min
Latching Loopback Mode = T1 (Disabled)
                        6.
                        7.
                        8. DS1 TX Level
                                                      = -7.5 \, dB
                        9. Customer Loss Indicator = AIS / CI
                       10. PRM Setting
                                                         None
                       11. Loop Atten Alarm Thres =
12. SNR Margin Alarm Thres =
                                                         34dB
                            SNR Margin Alarm Thres =
                                                         04dB
                       13. Remote Provisioning
                                                         Enabled
                               Selection:
```

Figure 8. Provisioning Screen

The Span Status Screen (**Figure 9**) provides quick access to status information for each HDSL4 receiver in the circuit.

The Detailed Status selection from the System Status Menu (**Figure 10**) displays the HDSL4 margin, attenuation, and estimated insertion loss. From this screen, Min/Max can be reset.

NOTE

The insertion loss reading shown on the Detailed Status Screen is an approximation that is valid for some loops. Caution should be used when using this value.

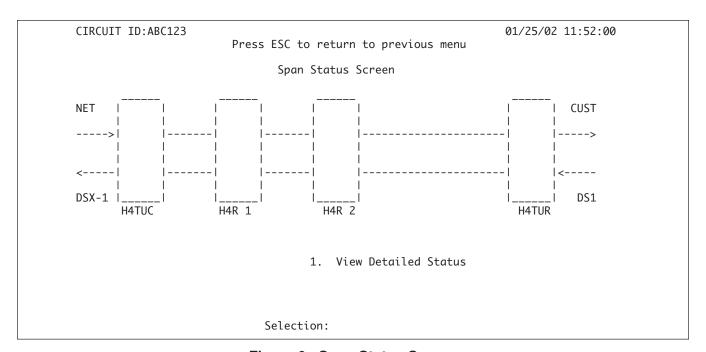


Figure 9. Span Status Screen

CIRCUIT ID:		ess ESC to	o return to	previous menu	01/2	5/02 11:52:00
		Detai ⁻	led Status	Screen		
Interface	MARGIN (CUR/MIN/MAX)	LOOP 1 ATTEN (CUR/MAX)	ESTIMATED INS. LOSS	MARGIN (CUR/MIN/MAX)	LOOP 2 ATTEN (CUR/MAX)	ESTIMATED INS. LOSS
H4TUC H4R1 NETW H4R1 CUST H4R2 NETW H4R2 CUST H4TUR	17/00/17 17/00/17 17/17/17 17/17/17 17/17/17 17/17/17	00/00 00/00 00/00 00/00 00/00 00/00	90 90 90 90 90 90	17/00/17 17/13/17 17/00/17 17/13/17 17/00/17 17/00/17	00/00 00/00 00/00 00/00 00/00 00/00	00 00 00 00 00 00
				Min/Max Performance Hi: n:	story	

Figure 10. Detailed Status Screen

The Loopback and Test Commands Screen (**Figure 11**) is used to invoke or terminate all available HDSL4 loopbacks. Each HDSL4 circuit component can be looped toward the network or customer from this screen.

The Performance History screens (**Figure 12** and **Figure 13**) display the historical HDSL4 and T1 performance data in several different registers. At each 15-minute interval, the performance

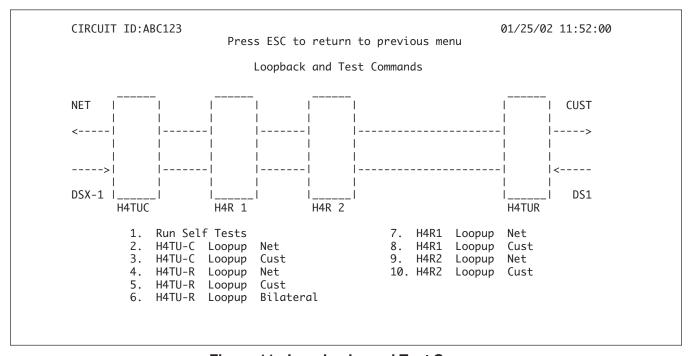


Figure 11. Loopbacks and Test Screen

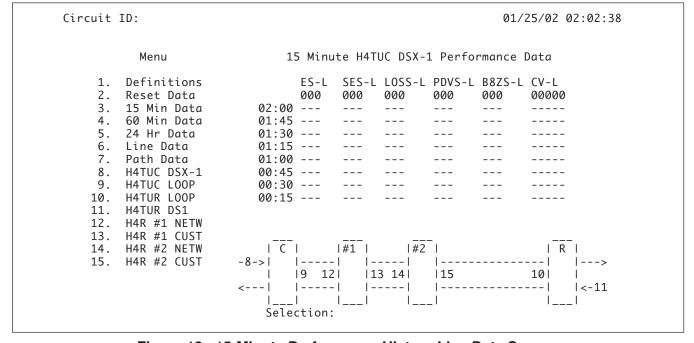


Figure 12. 15-Minute Performance History Line Data Screen

information is transferred to the 15-minute performance data register. This unit stores performance data in 15-minute increments for the last 24-hour period. At each 24-hour interval, the performance data is transferred into the 24-hour performance data registers. This unit stores up to 31 days of 24-hour interval data.

To view the performance data, select the desired module and interface. Line (L) and Path (P) Performance data can be viewed.

Abbreviations used in the Performance History screens are defined in Data Definition screens (**Figure 14** and **Figure 15**).

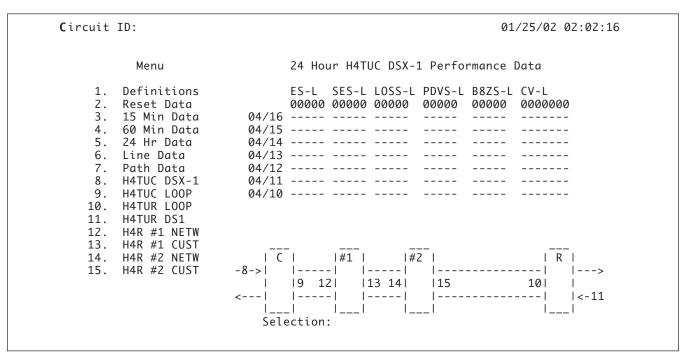


Figure 13. 24-Hour Performance History Line Data Screen

```
01/25/02 02:02:16
Circuit ID:
                          Performance Data Definitions
H4TUC, H4TUR, and H4R LOOP Related:
                                         HDSL4 Framina
  ES-L
          Errored Seconds
                                           CRC>=1 or LOSW>=1
  SES-L
          Severely Errored Seconds
                                           CRC >= 50 \text{ or } LOSW >= 1
 UAS-L
                                           >10 cont. SES-Ls
          Unavailable Seconds
DS1 and DSX-1 Line Related:
                                         Superframe and Extended Superframe
         Errored Seconds
  ES-L
                                           (BPV+EXZ)>=1 or LOS>=1
  SES-L
          Severely Errored Seconds
                                           (BPV+EXZ)>=1544 \text{ or } LOS>=1
         Loss of Signal Seconds
  LOSS-L
                                           LOS >= 1
  PDVS-L Pulse Density Violation Secs
                                           EXZ>=1; >7 zeros if B8ZS, >15 if AMI
  B8ZS-L
          B8ZS Seconds
                                           B8ZS coded signal received
          Code Violation Count
  CV-L
                                           (BPV+EXZ) count
NOTE: Reverse video indicates invalid data due to a terminal restart (or power
      cycle), a data register reset, or a system date or time change.
   N. Next
   P. Previous
                             Selection:
```

Figure 14. Performance Data Definitions Screen

Figure 16 illustrates the Scratch Pad, Circuit ID, and Time/Date Screen. The Scratch Pad data can be any alphanumeric string up to 50 characters in length. The Circuit ID can be any alphanumeric string up to 25 characters in length.

The time should be entered using military time (for example, enter 3:15 p.m. as "151500").

The date should be entered as MMDDYY (for example, enter January 02, 2003, as "010203").

```
Circuit ID:
                                                                   01/25/02 02:02:16
                           Performance Data Definitions
DS1 and DSX-1 Path Related:
                                        Superframe
                                                               Extended Superframe
  ES-P
          Errored Seconds
                                          FE>=1 or
                                                                 CRC>=1 or
                                           SEF>=1 or AIS>=1
                                                                  SEF>=1 or AIS>=1
  SES-P
          Severely Errored Seconds
                                          FE >= 8 \text{ or}
                                                                 CRC >= 320 \text{ or}
                                           SEF>=1 or AIS>=1
                                                                  SEF >= 1 \text{ or } AIS >= 1
  UAS-P
          Unavailable Seconds
                                          >10 cont. SES-Ps
                                                                 >10 cont. SES-Ps
  SAS-P
          SEF/AIS Seconds
                                          SEF>=1 or AIS>=1
                                                                 SEF>=1 or AIS>=1
  ES-PFE Far End Errored Seconds
                                                                 PRM bits G1-G6,SE,
                                          n/a
                                                                  or SL=1, or RAI
                                                                 CRC error count
          Code Violation Count
                                          FE count
NOTE: Under a UAS-P condition, ES-P and SES-P counts are inhibited.
      Under a SES-L or SES-P condition, the respective CV-L or CV-P count is
      inhibited.
                               Selection:
    P. Previous
```

Figure 15. Performance Data Definitions Screen (continued)

```
Current Scratch Pad:
New Scratch Pad =

New Circuit ID =

New Date = / / (MM/DD/YY)
New Time = : : (HH:MM:SS)

Press TAB to skip to next entry field.
```

Figure 16. Scratch Pad, Circuit ID, Time/Date Screen

This unit includes two terminal emulation modes. These modes are described on the Terminal Modes Menu, (**Figure 17**).

NOTE

Pressing CTRL T while on any screen to toggle between Manual and Real Time Terminal Modes.

The Manual Update Mode is used to manually update the provisioning option screens. This mode supports efficient print screen and log file utilities for storage of key provisioning parameters, alarm or performance history and current system status. "3 SPACES TO UPDATE" appears at the top of each screen. By pressing the spacebar 3 times to refresh the screen and to reflect the most current circuit conditions and provisioning options.

NOTE

When operating in Virtual Terminal Mode, the terminal baud rate should be 4.8 kbps or higher. The remote terminal session is automatically initiated if a terminal is connected to the H4TU-R, and the HDSL4 loops are in sync with the H4TU-C. When a remote terminal session is in progress, the screens are not accessible from the H4TU-C. Once a remote terminal session is terminated, the screens are available at the

H4TU-C. The remote terminal session is terminated by typing CTRL X on the terminal at the H4TU-R. Alternatively, if there is no keyboard input at the H4TU-R's terminal for a period of 5 minutes, the remote session will time out, and the screens will once again be available at the H4TU-C. After the 5 minute time out, the remote terminal session can be reinstated at the H4TU-R by pressing the space bar several times.

The default terminal emulation mode is the Real Time Update Mode (VT100). This mode provides real time updating of HDSL4 circuit conditions and provisioning options as changes occur. While in Real Time Update Mode, the unit is anticipating baud poll responses from the terminal.

CIRCUIT ID: 01/25/02 11:52:00

Press ESC to return to previous menu

TERMINAL MODES MENU

MANUAL UPDATE MODE:

- * You can print or log screens
- * No text is highlighted
- * "3 SPACES TO UPDATE" appears at the top of each screen, reminding you to press the spacebar 3 times to update the screen
- * There is a delay between screen changes & updates
- * After 30 min. of no interaction, a new baud rate search is begun
- * Ignores input until screen is finished printing.

REAL-TIME UPDATE MODE:

- * Faster of the two modes
- * You cannot print screens to a log file
- * Highlighting is enabled
- * Recommended for daily operation

Press CTRL+T to toggle update modes on any screen.

Figure 17. Terminal Modes Menu

The T1 Alarm History screen (**Figure 18**) provides the user with a detailed alarm history and events log for the T1 circuit. This screen includes a time, date, first/last occurrence, and count for each type of HDSL4 or T1 alarm. The HDSL4 Span History Screen (**Figure 19**) lists alarms that have occurred on the span between the H4TU-C and H4TU-R.

CIRCUIT I	D:ABC123	Press	ESC to return to previous menu	01/25/02	2 11:52:00
LOCATION	ALARM	FIRST	T1 Alarm History LAST	CURRENT	COUNT
	RED(LOS/LOF) YELLOW(RAI) BLUE(AIS)			OK OK OK	000 000 000
H4TU-R (DS1)	RED(LOS/LOF) YELLOW(RAI) BLUE(AIS)			OK OK OK	000 000 000
2. Fa	 Alarm cility Alarm an H4TUC to H	5. 4R1 C.	Span H4R1 to H4R2 Span H4R2 to H4TU-R Clear T1 Alarms election:		

Figure 18. T1 Alarm History Screen

HDSL4 Span History LOCATION ALARM FIRST LAST	CURRENT COUNT
SPAN C-H1 1 10S	
L2 LOS	OK 000 OK 000
H4TU-C L1 MRGN	OK 000
L2 MRGN	OK 000
H4R1 NET L1 MRGN	OK 000
L2 MRGN	OK 000
H4TU-C L1 ATTEN	OK 000
L2 ATTEN	OK 000
H4R1 NET L1 ATTEN	OK 000
L2 ATTEN	OK 000

Figure 19. HDSL4 Span History Screen

The Event History screen (**Figure 20**) provides a log history of HDSL4 circuit events.

The System PM/Screen Report option from the Main Menu offers four types of reports on performance monitoring. Selecting a report type will then display all the reports for that category on the screen at once, which is more efficient than stepping through the menus individually. See **Figure 21**.

```
CIRCUIT ID:
                                                 01/25/02 11:52:00
               Press ESC to return to previous menu
    Num Description of Event
                                                  Time
                                           Date
  1. H4TU-R Powered Up
                                           01/25/02 11:52:00
  2. H4TU-C Powered Up
                                           01/25/02 11:52:00
      Page Number: 1/ 1 Number of Events: 2
      -----
     'P' - Previous Page 'H' - Home 'R' - Reset Events
     'N' - Next Page
                     'E' - End
                      Selection:
```

Figure 20. Event History Screen

```
6. SCRATCH PAD, CKT ID, TIME/DATE
7. TERMINAL MODES
8. ALARM HISTORY
9. EVENT HISTORY
10. SYSTEM PM/SCREEN REPORT
11. VIRTUAL TERMINAL CONTROL

ENABLE DATA LOGGING NOW.

SELECT REPORT TYPE OR PRESS ESCAPE TO CANCEL:
1) FULL SYSTEM/HISTORY REPORT
2) CURRENT STATUS REPORT
3) SYSTEM CONFIGURATION REPORT
4) ALARM/EVENT HISTORY
```

Figure 21. System PM/Screen Report Screen

The Virtual Terminal Control screen (Figure 22) is used to access the unit on the far end of the circuit.

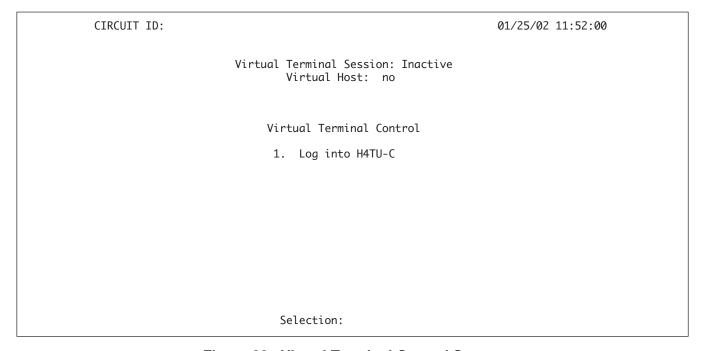


Figure 22. Virtual Terminal Control Screen

7. HDSL4 DEPLOYMENT GUIDELINES

The different segments of an HDSL4 circuit are defined in **Figure 23.**

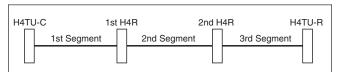


Figure 23. HDSL4 Circuit Segments

The ADTRAN HDSL4 system provides DS1-based services over loops designed to comply with the guidelines given below. These guidelines apply to the following circuit configurations:

- A single segment or an HDSL4 circuit with no H4Rs,
- A circuit having two segments (with one H4R), or
- A circuit having 3 segments (with two H4Rs).

The guidelines reflected herein are for worst-case scenarios, that is, for loops that contain a maximum amount of disturbers, noise, etc. Actual deployment guidelines may vary based on local policy. Please refer to those guidelines to ensure optimum performance.

Designing a circuit with loop attenuation greater than the recommended maximum loss (yellow zone) may result in compromised reliability of that loop. The guidelines are provided in numbers 1 through 9, which follow:

- 1. All loops are nonloaded only
- 2. Any single bridged tap is limited to 2 kft.
- 3. Total bridged tap length is limited to 2.5 kft.
- 4. Bridge tap within 1000 feet of units may affect performance of the circuit.
- 5. Loop Attenuation Limits. See **Table 4**.
- 6. DSL-Recommended Range Limits. See **Table 5** and **Table 6**.

Table 4. Attenuation Limits from DSL Assistant

	Recommended Maximum (DSL Assistant Green Zone)				
	Upstream	Downstream			
1st Segment	31 dB	33 dB			
2 nd /3 rd Segment	30 dB	30 dB			

Table 5. Range Limits: 26 Gauge Single Segment Only

26 Gauge	Recommended Maximum (DSL Assistant Green Zone)	
1st Segment	10.8 kft	
2 nd /3 rd Segment	10.55 kft	
3 rd Segment	10.55 kft (see note)	

Table 6. Range Limits: 24 Gauge Single Segment Only

24 Gauge	Recommended Maximum (DSL Assistant Green Zone)	
1st Segment	15.25 kft	
2 nd /3 rd Segment	15.05 kft	
3 rd Segment	15.05 kft (see note)	

ADTRAN provides an excellent computer-aided loop design tool called DSL Assistant that provides these loop loss design criteria automatically. The color-coded loop attenuation results provide green when the loss is at or below the recommended maximum loss, yellow if the loss is more than recommended but below the absolute maximum and red if the loss exceeds the T1.418 standard based absolute maximum.

NOTE

In three segment circuits (two H4Rs), individual segment resistance values must be verified. See paragraph Number 7 below.

7. Resistance Values. See Table 7.

Each of the three segments associated with span powering two H4Rs and a H4TU-R must satisfy the DC resistance budgets in addition to the recommended insertion loss and loop attenuation requirements. In general, 22 and 19 AWG segments will be restricted by their loop attenuation while the DC resistance will restrict the segment reach for 26 and 24 AWG. When designing a dual H4R loop, the first segment should have lower DC resistance than the second segment.

Single H4R spans do not require any restriction due to DC resistance.

NOTE

A circuit that otherwise meets attenuation and insertion loss requirements for cable reach will encounter span powering problems if resistance values are excessive.

The segment resistance (Ω_{segment}) is determined using the equation provided below:

$$\boldsymbol{\Omega}_{\text{segment}} = \boldsymbol{L}_{26} * \boldsymbol{\Omega}_{26} + \boldsymbol{L}_{24} * \boldsymbol{\Omega}_{24} + \boldsymbol{L}_{22} * \boldsymbol{\Omega}_{22} + \boldsymbol{L}_{19} * \boldsymbol{\Omega}_{19}$$

where : $L_{\#}$ is the length of # AWG cable (kft, excluding bridge taps), $\Omega_{\#}$ is the D.C. Resistance of # AWG cable

Table 7 list single pair cable DC resistance values to be used in equation above.

Table 7. Single Pair Cable DC Resistance Value

Resistance (ohms/kft)				
AWG	70°F	90°F°*	120°F	140°F**
19	16.465	17.183	18.261	18.979
22	33.006	34.446	36.606	38.046
24	52.498	54.789	58.225	60.516
26	83.475	87.117	92.581	96.223

^{*} Interpolated between 70°F and 120°F data

^{**} Extrapolated from 70°F and 120°F data

Once the resistance of each segment is confirmed, refer to **Figure 24** to decide if the H4TU-C is capable of span powering two H4Rs and one H4TU-R. Follow these steps to utilize the graph shown in Figure 24:

- a) Find the line on the graph which represents the known third segment resistance. These are the lines running diagonally across the graph labeled 300 1100 ohms. This line represents the upper limit for two H4Rs plus H4TU-R span powering.
- b) Find the first segment resistance on the vertical
- c) Find the second segment resistance on the horizontal axis.

- d) Find the instance where the two points from Steps b and c meet on the graph.
- e) The point found in step d must be below the upper limit line defined by the third segment measurement (Step a). If the instance where these two points is above this line, the H4TU-C cannot span power two H4Rs and the H4TU-R.

Note that these measurements represent only one of the two HDSL4 pairs.

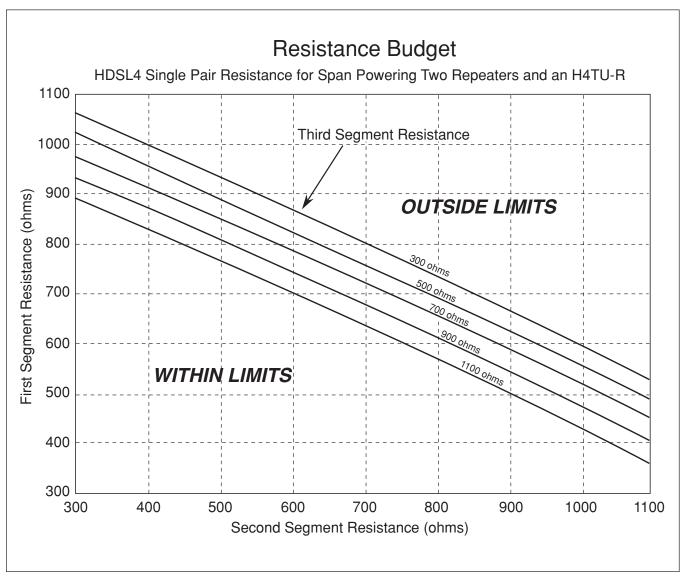


Figure 24. Resistance Budget Span Powering Two Repeaters

An example problem is illustrated in **Figure 25**. For this example, begin with three known measurements: 600 ohm first segment resistance, 700 ohm second segment resistance, and 900 ohm third segment resistance. Refer to Figure 25 and the following steps to solve the example problem:

- a) Find the 900 ohm third segment resistance line on the graph. This line is depicted in bold in Figure 25. This line is the upper span power limit.
- b) Find the 600 ohm first segment resistance point on the vertical axis.
- c) Find the 700 ohm second segment resistance point on the horizontal axis.

- d) Find the instance on the graph where the points from Steps b and c meet.
- e) If this point is below the bold line defined in Step a, the circuit is capable of span powering two H4Rs and one H4TU-R.

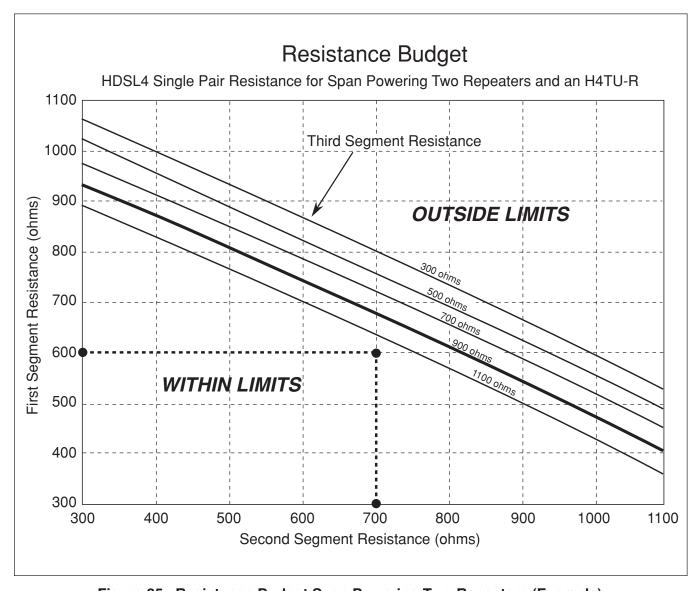


Figure 25. Resistance Budget Span Powering Two Repeaters (Example)

8. Insertion Loss limits. See Table 8.

The asymmetric nature of the HDSL4 TC PAM line code necessitates that insertion loss be verified at 4 separate frequencies. Verifying at only 196 kHz will not insure proper circuit performance.

9. Simplified Loop Qualification Procedure. See **Table 9** and **Table 10**.

For each of the four (or three for second and third segment) measured insertion loss values, compute the difference between the recommended maximum loss and the measured loss (rec minus measured). If all differences are positive, then the loop meets the performance criteria. If any one of the difference values is negative (measured loss is more than rec maximum loss), then the sum of the differences must be at least +3dB for 1st segment or +1 dB for 2nd/3rd segment. If neither of these criteria is satisfied, then the loop is suspect and may not provide robust HDSL4 deployment.

Table 8. HDSL4 Loop Insertion Loss Values

(Based upon 26 AWG cable)

	Recommended Maximum		
Frequency (KHz)	1st Segment Loss (dB)	2 nd /3 rd Segment Loss (dB)	
50	31.5	29.9	
80	35.3	33.5	
130	39.1	37.1	
196	43.0	N/A	

Table 9. Single Span and First Segment of Repeatered Loop (Based upon 11.1 kft. of 26 AWG cable)

Frequency (kHz)	Recommended Maximum Loss (dB)	Measured Loss (dB)	Delta Loss (dB) (Max-Meas)
50	31.5		
80	35.3		
130	39.1		
196	43.0		
		¹ Sum Delta Loss =	

If any single frequency insertion loss exceeds the maximum loss (delta loss < 0), then the sum of the four delta loss values must be > 3.0 dB

Table 10. Second or Third Segment of Repeatered Loop
(Based upon 10.5 kft. of 26 AWG cable)

Frequency (kHz)	Recommended Maximum Loss (dB)	Measured Loss (dB)	Delta Loss (dB) (Max-Meas)
50	29.9		
80	33.5		
130	37.1		
		¹ Sum Delta Loss =	

If any single frequency insertion loss exceeds the maximum loss (delta loss < 0), then the sum of the four delta loss values must be > 3.0 dB

8. TROUBLESHOOTING PROCEDURES

Use **Table 11** to troubleshoot the ADTRAN H4TU-R.

9. MAINTENANCE

The ADTRAN H4TU-R requires no routine maintenance. In case of equipment malfunction, use the front panel bantam jack and/or DB-9 connector to help locate the source of the problem.

ADTRAN does not recommend that repairs be performed in the field. Repair services may be obtained by returning the defective unit to the ADTRAN Customer and Product Service (CAPS) Department.

10. PRODUCT SPECIFICATIONS

Table 12 lists the H4TU-R specifications.

11. WARRANTY AND CUSTOMER SERVICE

ADTRAN will replace or repair this product within the warranty period if it does not meet its published specifications or fails while in service. Warranty information can be found at www.adtran.com/warranty.

USA and Canadian customers can also receive a copy of the warranty via ADTRAN's toll-free faxback server at 877-457-5007.

- Request Document 414 for the *U.S. and Canada Carrier Networks Equipment Warranty*.
- Request Document 901 for the *U.S. and Canada Enterprise Networks Equipment Warranty*.

Refer to the following subsections for sales, support, CAPS requests, or further information.

ADTRAN Sales

Pricing/Availability: 800-827-0807

ADTRAN Technical Support

Pre-Sales Applications/Post-Sales Technical Assistance: 800-726-8663

Standard hours: Monday - Friday, 7 a.m. - 7 p.m. CST Emergency hours: 7 days/week, 24 hours/day

ADTRAN Repair/CAPS

Return for Repair/Upgrade: (256) 963-8722

Repair and Return Address

Contact Customer and Product Service (CAPS) prior to returning equipment to ADTRAN.

ADTRAN, Inc. CAPS Department 901 Explorer Boulevard Huntsville, Alabama 35806-2807

Table 11. Troubleshooting Guide

Condition: All front panel indicators are off.

Solutions:

- 1. Make sure the H4TU-R is properly seated in the housing.
- 2. Verify that the H4TU-C is delivering sufficient voltage to the loops.
- 3. If Steps 1 and 2 pass, but front panel indicators are still off replace the H4TU-R.

Condition: DSL 1/DSL 2 LED is Red.

Solutions:

- 1. Verify that loss (attenuation) on Detailed System Status screen is < 35 dB on the first segment of the circuit and < 31 dB on the second and third segments of the circuit.
- 2. Verify that the loops conform with HDSL4 Deployment Guidelines. See Section 7.
- 3. Verify that noise on the HDSL4 loops is within acceptable limits.
- 4. If steps 1 through 3 pass and LED is Red, replace the H4TU-R.

Table 12. ADTRAN T200 H4TU-R Specifications

Specification	Description		
Loop Interface			
Modulation Type Mode Number of Pairs Line Rate Baud Rate Loop Loss Bridged Taps Performance H4TU-C Transmit Power (Data) Level H4TU-C Transmit Power (Activation) Level Input Impedence Maximum Loop Resistance Return Loss			
Network	Interface		
DS1 Transmit Level DSX-1 Line Buildout DSX-1 Line Code	0 dB (default), -7.5 dB, -15 dB 0-133 ft. ABAM (default) 133-266 ft. ABAM 266-399 ft. ABAM 399-533 ft. ABAM 533-655 ft. ABAM B8ZS (default), AMI		
	wer (1222401L1) and H4R (1221445L1)		
H4TU-R Power Dissipation Span Power Fusing	3.8 watts -190 VDC (from H4TU-C) Class A2 compliant, GFI current limited at < 5 mA, loop current limited at 150 mA 1.00 A (not field-replacable)		
Cl	ock		
Clock Sources Internal Clock Accuracy	Internal, DSX-1 derived (with HDSL4 frame bit stuffing) ±25 ppm (exceeds Stratum 4), meets T1.101 timing requirements		
Te	ests		
Diagnostics	Self-Test, Local Loopback (H4TU-C), Remote Loopback (H4TU-R)		
Phy	sical		
T200 Office Repeater Shelf-Mounted Dimensions Weight	5.5 in. high, x 0.7 in. wide, x 6.0 in. deep < 1 lb.		
Enviro	onment		
Operating Temperature (Standard) Storage Temperature	-40°C to +70°C -40°C to +85°C		
Comp	liance		
GR-108 GR-63 ANSI T1.418	50950 9-CORE -CORE -2001, Issue 2 (DS1 Interface)		
Part Number			
T200 H4TU-R	1222426L1		

Appendix A HDSL4 Loopbacks

HDSL4 MAINTENANCE MODES

This appendix describes operation of the HDSL4 system with regard to detection of in-band and ESF facility data link loopback codes.

Upon deactivation of a loopback, the HDSL4 system will synchronize automatically.

Loopback Process Description

In general, the loopback process for the HDSL4 system elements is modeled on the corresponding DS1 system process. Specifically, the H4TU-C loopback is similar to an Intelligent Office Repeater loopback, and the H4TU-R loopbacks are similar to a T1 Smart Jack loopback.

The unit can detect the loopback activation or deactivation code sequence *only* if an error rate of 1E⁻⁰³ or better is present.

Loopback Control Codes

A summary of network and customer control sequences is given in **Table A-1** and **Table A-2**.

NOTE

In all control code sequences presented, the in-band codes are shown left-most bit transmitted first, and the ESF data link codes with right-most bit transmitted first.

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Table A-1. HDSL4 Loopback Control Codes

Туре	Source 1 Code 2,3 Name
Abbreviated	(N)
	(N)
	(N)2in6 (110000)
	Loopback data from network toward network in first HRE. (N)
	Loopback data from network toward network in second HRE.
	(C)6in7 (1111110)
	Loopback data from customer toward customer in HTU-C.
	(C)
	(C)4in6 (111100)
	Loopback data from customer toward customer in first HRE.
	(C) 5in6 (111110) Loopback data from customer toward customer in second HRE.
	200000000000000000000000000000000000000
Wescom	(N) FF1E (1111 1111 0001 1110)
	Loopback data from network toward network at HTU-C. (C)
	Loopback data from customer toward customer at HTU-C.
	(N) FF04 (1111 1111 0000 0100)
	Loopback data from network toward network at HRE1.
	(N) FF06 (1111 1111 0000 0110) Loopback data from network toward network at HRE2.
	(C)
	Loopback data from customer toward customer at HRE1.
	(C)
	(N) FF02 (1111 1111 0000 0010)
	Loopback data from network toward network at HTU-R.
	(C)3F02 (0011 1111 0000 0010)
	Loopback data from customer toward customer at HTU-R. (C)
	Loopback data from customer toward customer at HTU-R.
	(N) lin6 (100000)
	Loopback data from network toward network at HTU-R.
	(N) FF48 (ESF-DL) (1111 1111 0100 1000) Loopback data from network toward network at HTU-R.
	(N/C) 1in3 (100)
	Loop down everything.
	(N/C) FF24 (ESF-DL) (1111 1111 0010 0100) Loop down everything.
	Loop down everydding.

A-1 The Source column indicates which side of the interface the control codes are sent from. For example, an (N) indicates a network sourced code while a (C) indicates a customer sourced code.

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A-2 All codes are in-band unless labeled ESF-DL

A-3 All codes listed above must be sent for a minimum of 5 seconds for them to be detected and acted upon.

Table A-2. Loopback and Control Codes

Function	Code	Response
ARM (in-band)—also known as 2-in-5 pattern	11000 (binary)	If the pattern is sent from the network, the units will arm, and the H4TU-R will loop up if NIU Loopback is enabled.
ARM (ESF Data Link)	FF48 (hex) or 1111 1111 0100 1000 (binary) sent in the Facility Data Link	If the pattern is sent from the network, the units will arm, and the H4TU-R will loop up if NIU Loopback is enabled. If the pattern is sent from the customer, the units will arm.
Disarm (in-band)—also known as 3-in-5 pattern	11100 (binary)	When sent from the network or customer, all units are removed from the armed state and loopbacks will be released.
Disarm (ESF Data Link)	FF24 (hex) or 1111 1111 0010 0100 (binary) sent in the Facility Data Link	When sent from the network or customer, all units are removed from the armed state and loopbacks will be released.
H4TU-C Loop Up ^{1,2}	D3D3 (hex) or 1101 0011 1101 0011 (binary)	If armed, the H4TU-C will loop up, 2 seconds of AIS (all ones) will be transmitted, the looped data will be sent for 5 seconds, and then a burst of 231 logic errors will be injected. The burst of 231 logic errors will continue every 20 seconds as long as the D3D3 pattern is detected. When the pattern is removed, the unit will remain in loopback. If the pattern is reinstated, the injection of 231 logic errors will continue every 20 seconds.
Loop Down w/o Disarm	9393 (hex) or 1001 0011 1001 0011 (binary)	When sent from the network, all units currently in loopback will loop down. Armed units will not disarm. In order to behave like a smartjack, the H4TU-R will not loop down from a network loopback in response to the 9393 pattern if NIU Loopback is enabled.
Loopback Query ¹	D5D5 (hex) or 1101 0101 1101 0101 (binary)	When the pattern is sent from the network, logic errors will be injected toward the network to indicate a loopback is present toward the network. The number of errors injected is determined by the nearest unit that is in loopback. As long as the pattern continues to be sent, errors are injected again every 20 seconds. 231 errors = H4TUC, 10 errors = H4R #1, 200 errors = H4R #2, 20 errors = H4TU-R

¹Units must be armed with 11000b or FF48h before this code will work.

Note: All codes listed above must be sent for a minimum of 5 seconds to be detected and acted upon.

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²Loopback and error injection will only occur if the in-band code is received by the unit that is to go into loopback. In other words, if another loopback blocks the in-band code from being transmitted to the unit that is to go into loopback, loopback and error injection will not occur.

Table A-2. Loopback and Control Codes (continued)

Function	Code	Response
Loopback Time Out Override ¹	D5D6 (hex) or 1101 0101 1101 0110 (binary)	If the units are armed or a unit is currently in loopback when this pattern is sent from the network, the loopback time out will be disabled. As long as the units remain armed, the time out will remain disabled. When the units are disarmed, the loopback time out will revert to the previous loopback time out setting. If any element is in network loopback a bit error confirmation will be sent. H4TU-C231 bps H4R1110 bps H4R22200 bps H4TU-R20 bps
Span Power Disable ¹	6767 (hex) or 0110 0111 0110 0111 (binary)	If the units are armed and 6767 is sent from the network, the H4TU-C will disable span power. If the pattern is sent from the network, the span power will be disabled as long as 6767 pattern is detected. Once the pattern is no longer received, the H4TU-C will reactivate span power. All units will then retrain and return to the disarmed and unlooped state.
First H4R Loop Up ^{1,2}	C741 (1100 0111 0100 0001)	If one or more H4Rs are present, the H4R closest to the H4TU-C will loop up toward the network, 2 seconds of AIS (all ones) will be transmitted, the looped data will be sent for 5 seconds, and then a burst of 10 logic errors will be injected. The burst of 10 logic errors will continue every 20 seconds as long as the C741 pattern is detected. When the pattern is removed, the unit will remain in loopback. If the pattern is reinstated, the injection of 10 logic errors will continue every 20 seconds.
Second H4R Loop Up ^{1,2}	C754 (1100 0111 0101 0100)	If two H4Rs are present, the second H4R from the H4TU-C will loop up toward the network, 2 seconds of AIS (all ones) will be transmitted, the looped data will be sent for 5 seconds, and then a burst of 200 logic errors will be injected. The burst of 200 logic errors will continue every 20 seconds as long as the C754 pattern is detected. When the pattern is removed, the unit will remain in loopback. If the pattern is reinstated, the injection of 200 logic errors will continue every 20 seconds.
H4TU-R Address 20 for Extended Demarc ^{1,2}	C742 (1100 0111 0100 0010)	If armed, the H4TU-R will loop up toward the network, 2 seconds of AIS (all ones) will be transmitted, the looped data will be sent for 5 seconds, and then a burst of 20 logic errors will be injected. The burst of 20 logic errors will continue every 10 seconds as long as the C742 pattern is detected. When the pattern is removed, the unit will remain in loopback. If the pattern is reinstated, the injection of 20 logic errors will continue every 20 seconds.

¹Units must be armed with 11000b or FF48h before this code will work.

Note: All codes listed above must be sent for a minimum of 5 seconds to be detected and acted upon.

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²Loopback and error injection will only occur if the in-band code is received by the unit that is to go into loopback. In other words, if another loopback blocks the in-band code from being transmitted to the unit that is to go into loopback, loopback and error injection will not occur.